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FEB 26 2007**FAX TRANSMISSION****DATE:** February 26, 2007**PTO IDENTIFIER:** Application Number 09/894,203
Patent Number**Inventor:** Ichiro TOMOHIRO**MESSAGE TO:** US Patent and Trademark Office**FAX NUMBER:** (571) 273-8300**FROM:** MORRISON & FOERSTER LLP

Adam Keser

PHONE: (703) 760-7301**Attorney Dkt. #:** 299002053200**PAGES (Including Cover Sheet):** 10**CONTENTS:** Transmittal (1 page)
Request for Reconsideration (7 pages)
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PTO/SB/97 (09-04)

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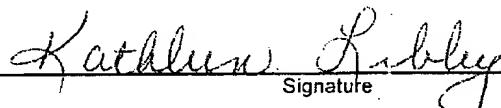
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Application No. (if known): 09/894,203

Attorney Docket No. 299002053200

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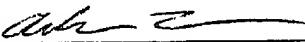
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		Application Number	09/894,203
		Filing Date	June 28, 2001
		First Named Inventor	Ichiro TOMOH:RO
		Art Unit	2136
		Examinee Name	D. G. Cervetti
Total Number of Pages in This Submission	8	Attorney Docket Number	299002053200

ENCLOSURES (Check all that apply)

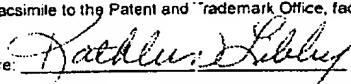
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<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input type="checkbox"/> Status Letter
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

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Printed name	Adam Keser		
Date	February 26, 2007	Reg. No.	54,217

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Docket No. 299002053200

(PATENT)

Client Reference No.: F5-01965232/01R00118-1US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Ichiro TOMOHIRO

Application No. 09/894,203

Confirmation No. 7078

Filed: June 28, 2001

Art Unit: 2136

For: SEMICONDUCTOR STORAGE DEVICE

Examiner: David G. Cervetti

REQUEST FOR RECONSIDERATION

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the final Office Action dated October 24, 2006, and further in response to the Advisory Action mailed February 12, 2007, please reconsider this application in light of the following remarks.

REMARKS

Claims 1-10 stand rejected under 35 USC 102(b) on Vicard (U.S. Patent No. 5,708,715).
Applicant respectfully traverses this rejection.

Applicant previously argued that Vicard discloses a semiconductor memory incorporating a conventional data tamper prevention circuit similar to that depicted in applicant's Figure 3, and therefore does not disclose or suggests "at least one memory region, each one of said at least one memory region being provided in the at least one memory cell array block, *for storing the security release key; [and] at least one non-volatile storage means for storing a security registration lock*"

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